# IEEE Reference Report

## Overview

- Type detected: journal article

- DOI: Not available

- Primary source for DOI: Unknown

- Trusted online match: Yes

Match rationale: Strict title/author/venue match from

Sources searched: Crossref, OpenAlex, PubMed, Semantic Scholar

## Field Verification

Fields matched authoritative sources: authors, doi, issue, pages, title, volume, year

Fields needing attention: journal\_abbrev, journal\_name

## Corrections Applied

- journal\_abbrev: MISSING → Journal not found (by: Unknown)

- title: CMOS programmable delay vernier → CMOS Programmable Delay Vernier. (by: Unknown)

- authors: M. Goto, J. O. Barnes → Masahide Goto, James Barnes, Roxanne Owens (by: Unknown)

- pages: 45-58 → 51-58 (by: Unknown)

- year: 2000 → 1994 (by: Unknown)

- volume: MISSING → 45 (by: Unknown)

- issue: MISSING → 5 (by: Unknown)

## Provenance (Source per Field)

- title: CMOS Programmable Delay Vernier. (source: Unknown)

- authors: Masahide Goto, James Barnes, Roxanne Owens (source: Unknown)

- journal\_name: Hewlett Journal (source: Unknown)

- volume: 45 (source: Unknown)

- issue: 5 (source: Unknown)

- pages: 51-58 (source: Unknown)

- year: 1994 (source: Unknown)

- month: 10 (source: Unknown)

## Online Evidence (links)

- Crossref (DOI): https://doi.org/10.1109/dcas.2005.1611176

- Crossref (DOI): https://doi.org/10.1109/icev59168.2023.10329724

- Crossref (DOI): https://doi.org/10.1109/iscas.2004.1328308

- Crossref (DOI): https://doi.org/10.1063/1.4985542

- Crossref (DOI): https://doi.org/10.1109/icce.2006.1598465

- OpenAlex: https://openalex.org/W3184175586

- Semantic Scholar: https://www.semanticscholar.org/paper/0bc98dad3537abbaac877ce70c0a738eabf3cc46

- Semantic Scholar (DOI): https://www.semanticscholar.org/doi/10.1109/DCAS.2005.1611176

- Semantic Scholar: https://www.semanticscholar.org/paper/c66198614cd1e39c5cde09610d69c2804a9c99a9

- Semantic Scholar (DOI): https://www.semanticscholar.org/doi/10.1109/TCSII.2014.2345289

- Semantic Scholar: https://www.semanticscholar.org/paper/d366c2bdefe93d53a6c751ff7702b299608f625f

- DOI: https://doi.org/10.1109/tcsii.2014.2345289

- Semantic Scholar (DOI): https://www.semanticscholar.org/doi/10.1109/TIE.2017.2669883

- Semantic Scholar: https://www.semanticscholar.org/paper/228655a3d53eea537e4d4c3b4d39195587d0ce44

- DOI: https://doi.org/10.1109/tie.2017.2669883

- Semantic Scholar (DOI): https://www.semanticscholar.org/doi/10.1117/12.631065

- Semantic Scholar: https://www.semanticscholar.org/paper/825ee5b4784b191fe146262999faa0da345a3b49

- DOI: https://doi.org/10.1117/12.631065

- PubMed: https://pubmed.ncbi.nlm.nih.gov/33499338/

- DOI: https://doi.org/10.3390/s21030743

## Journal Abbreviation Check

NLM Catalog ISO Abbrev: (not verified or not applicable)

## Formatting Strategy

LLM-based formatting applied successfully

## Final Formatted Reference

M. Goto, J. Barnes, and R. Owens, "CMOS Programmable Delay Vernier," \*Hewlett Journal\*, vol. 45, no. 5, pp. 51–58, Oct. 1994.

## Data Quality Warnings

- Missing DOI for an article/conference reference

## Reproducibility

Fingerprint: 38e3fa084fb30043bac132d4017f4d780cc359ef0dba1907f8653e7e2db5bdbc